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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. A.  
Fathimulla, et al.

Serial No.: 10/764,938

For: SILICON-ON-  
INSULATOR WAFER FOR RF  
INTEGRATED CIRCUIT

Filed: January 26, 2004

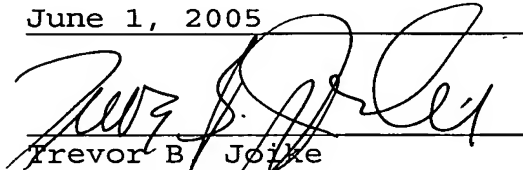
Group Art Unit: 2814

Examiner: L. Pham

Confirmation No.: 9312

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Attorney for Applicants

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Commissioner for Patents  
P.O. Box 1450  
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Dear Sir:

ELECTION RESPONSIVE TO 05/03/05 RESTRICTION REQUIREMENT

Claims 1-6 and 22-31 are pending in the  
application. The Examiner has required restriction  
between claims 1-6 directed to a device classified in  
257/347 and claims 22-31 directed to a method classified  
in 438/311.

Applicant hereby elects claims 1-6.

CONCLUSION

In view of the above, examination of the  
present application is respectfully requested.

Respectfully submitted,

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June 1, 2005